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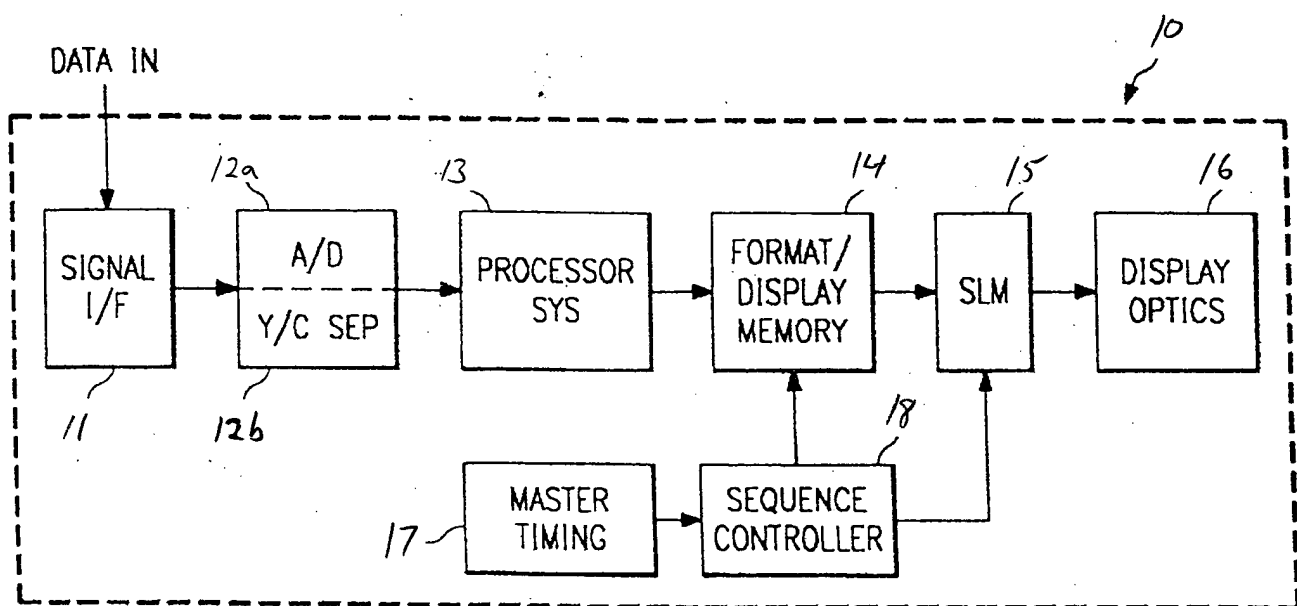


Fig. 1 Prior Art

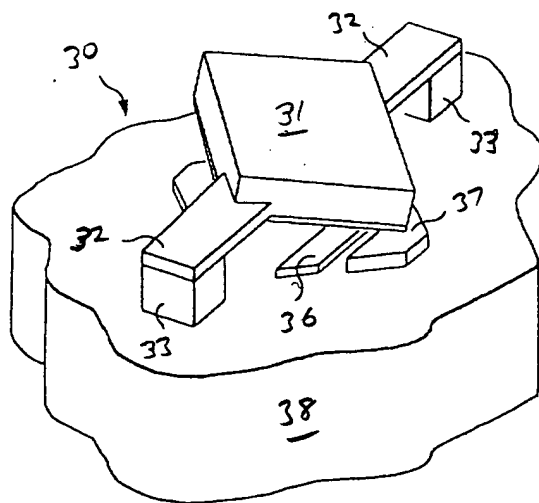
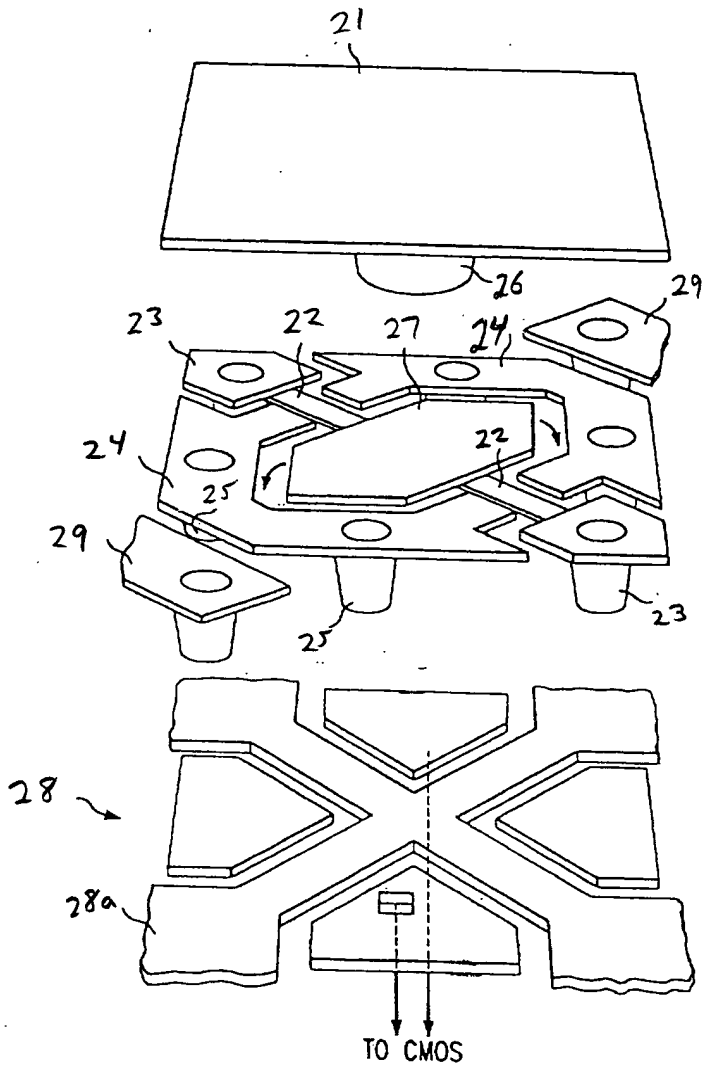


Fig. 4a Prior Art

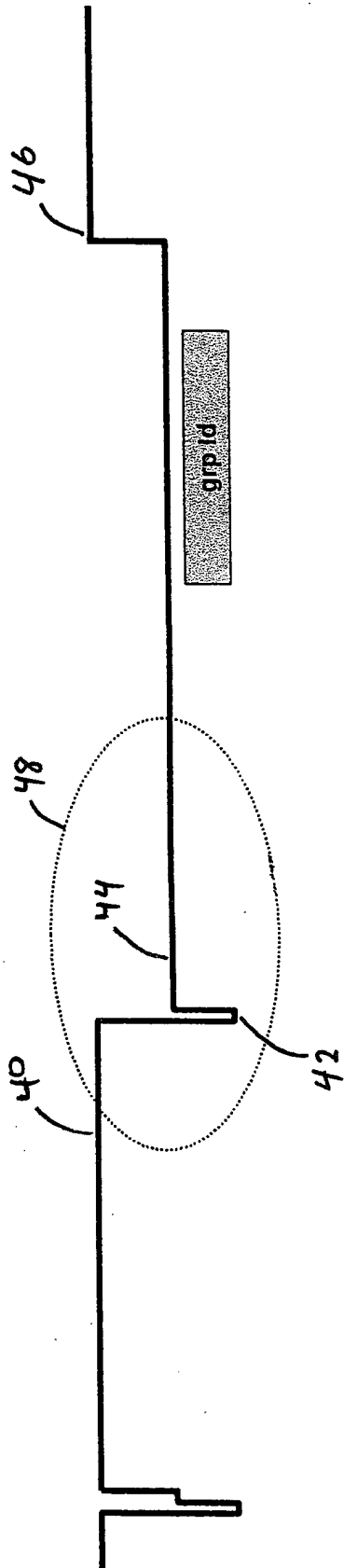
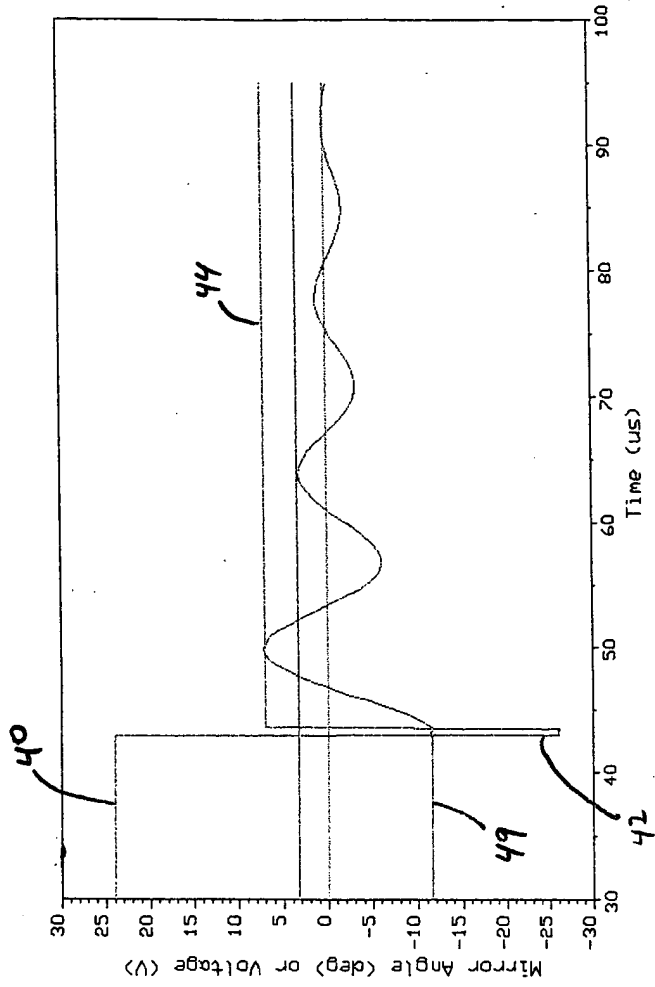
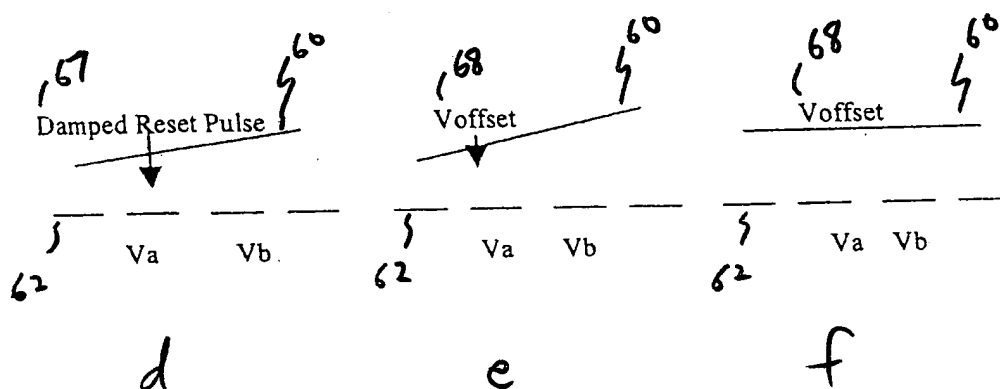
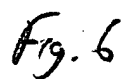


Fig 4b
Prior Art



The diagram shows a digital signal waveform. It starts with a low pulse labeled "Reset for Short bit". This is followed by a high pulse labeled "50" with a lightning bolt symbol. Then, there is a low pulse labeled "RNR reset" with a handwritten "52" below it. This is followed by a high pulse labeled "DD, DPW" with an upward arrow and a handwritten "54" below it. Then, there is another low pulse labeled "Damping Pulse width" with an upward arrow and a handwritten "56" below it. This is followed by a high pulse labeled "long RNR Tbo" with a handwritten "58" above it. Finally, the signal returns to a high level labeled "60".



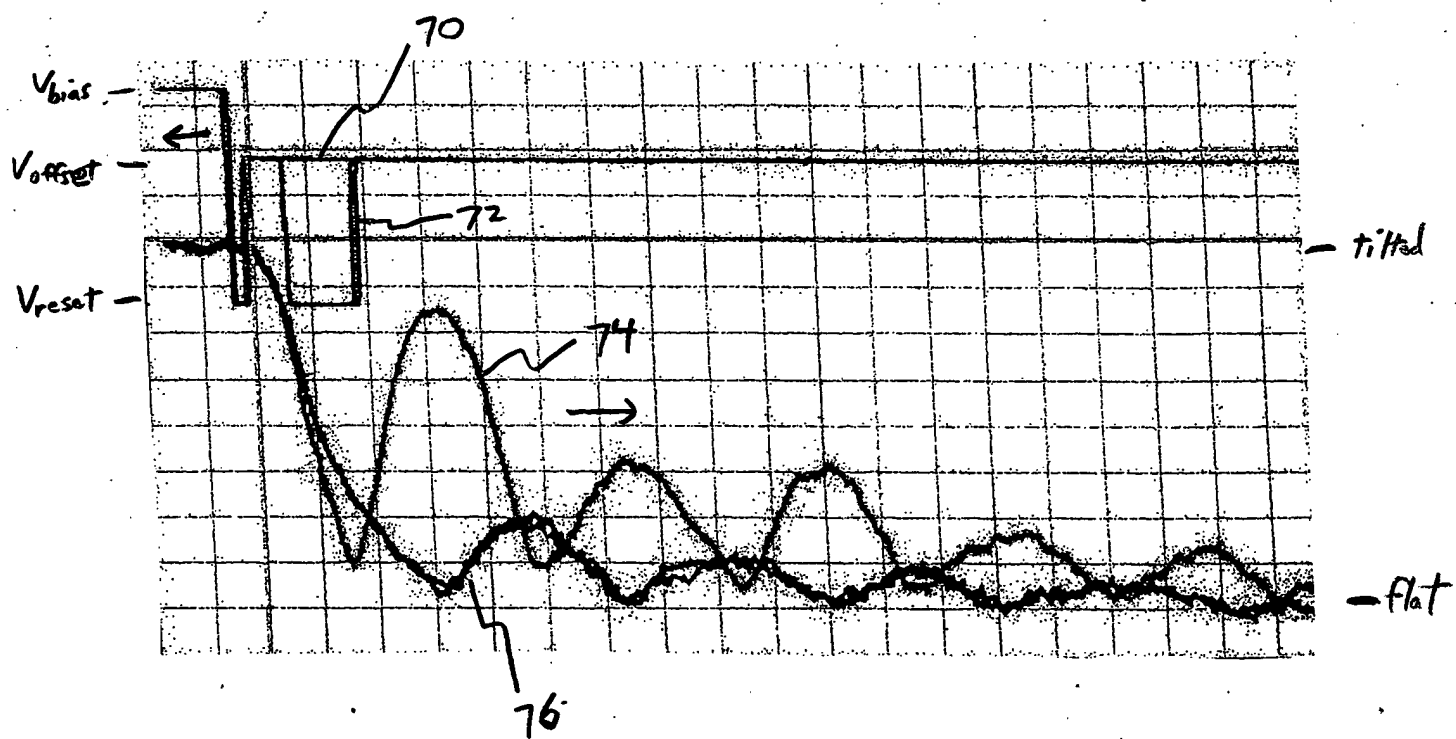


Fig. 7

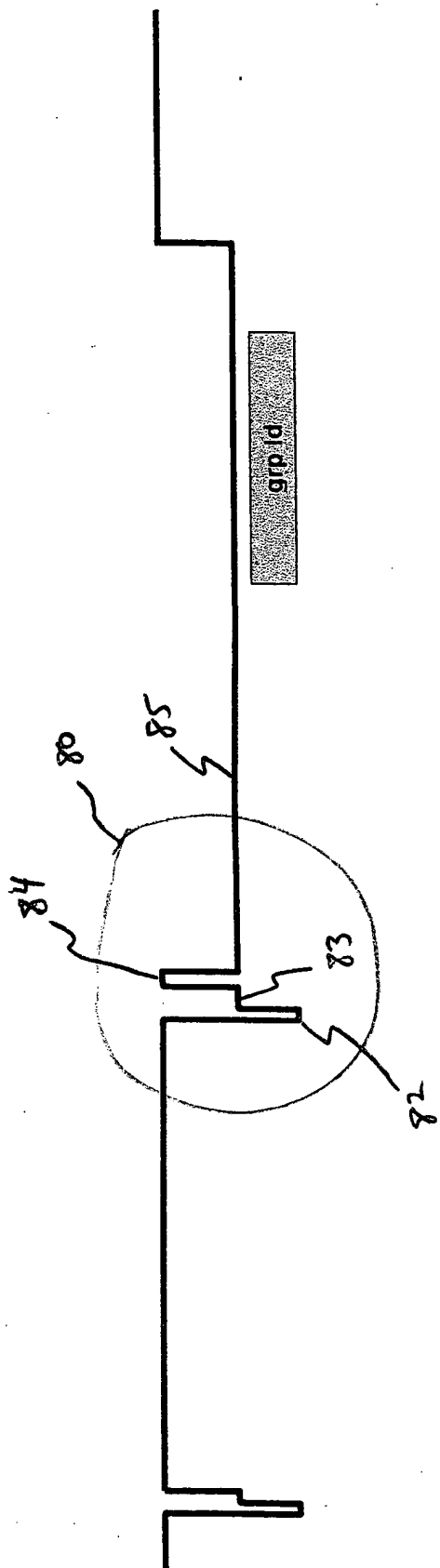


Fig. 8a

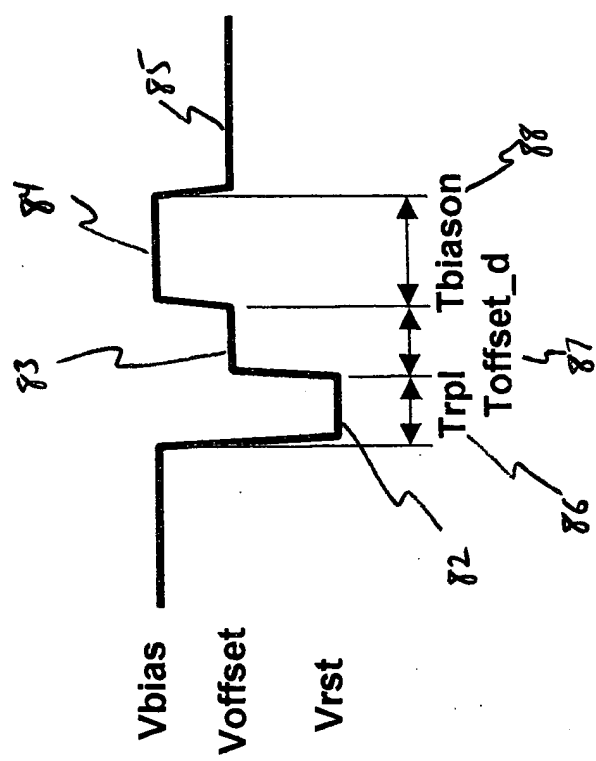
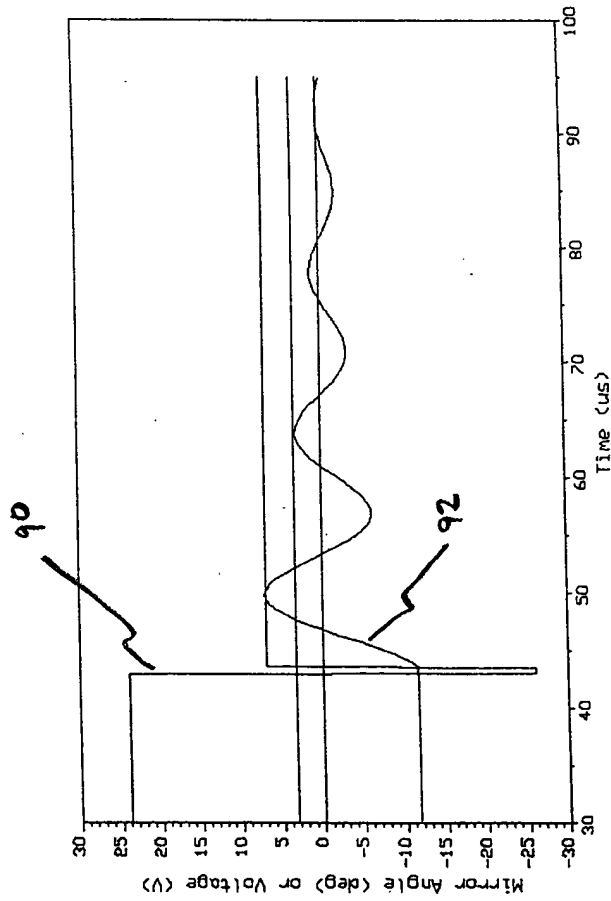


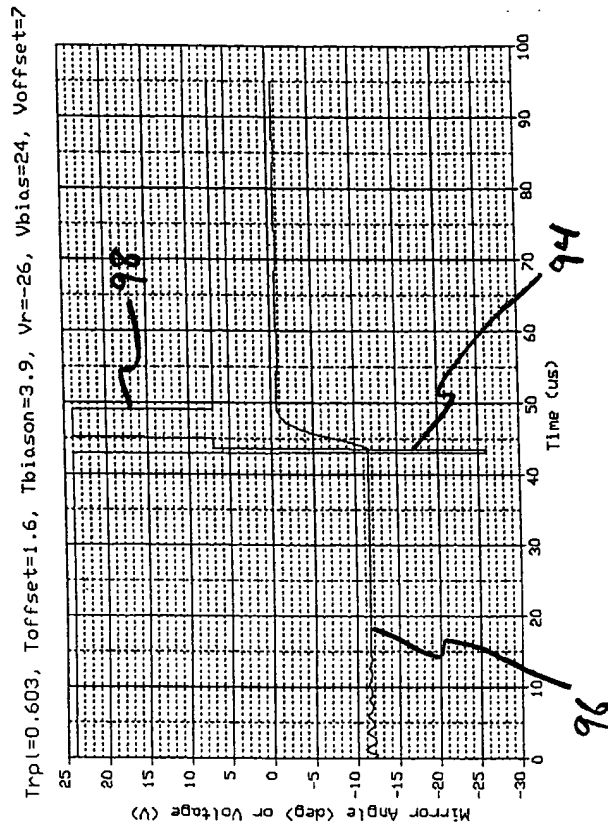
Fig. 8b

Damped-release vs. Reset-release model



Reset-release

Fig. 9a



Damped-release

Fig. 9b

Current Reset-and-release, black video content

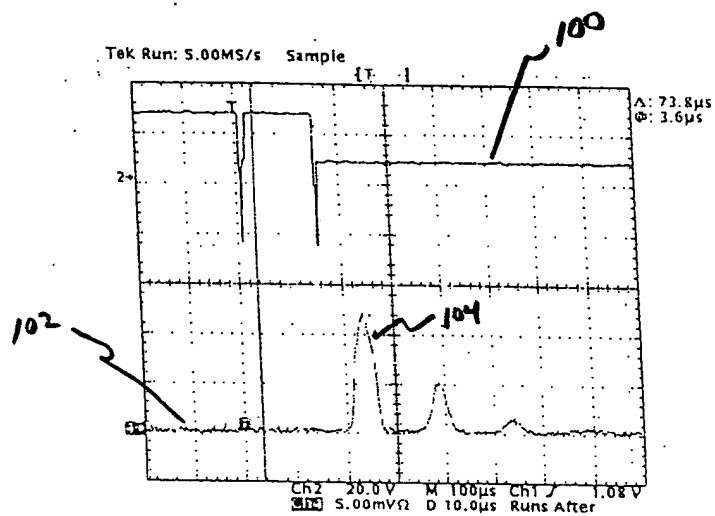


Fig. 10a

Damped-release, black video content

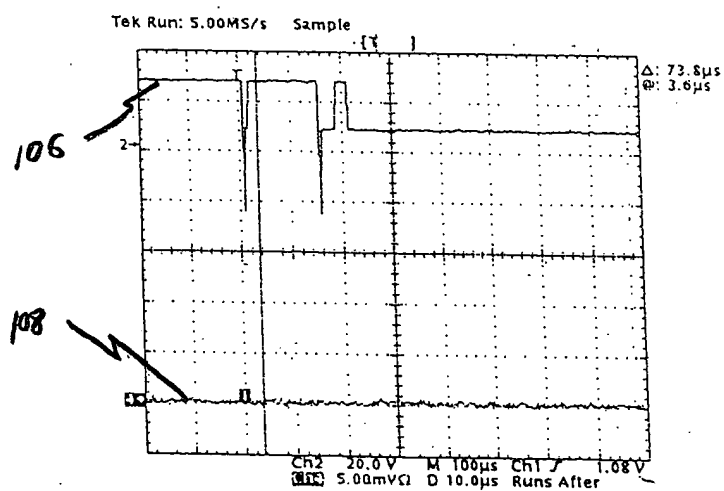


Fig. 106

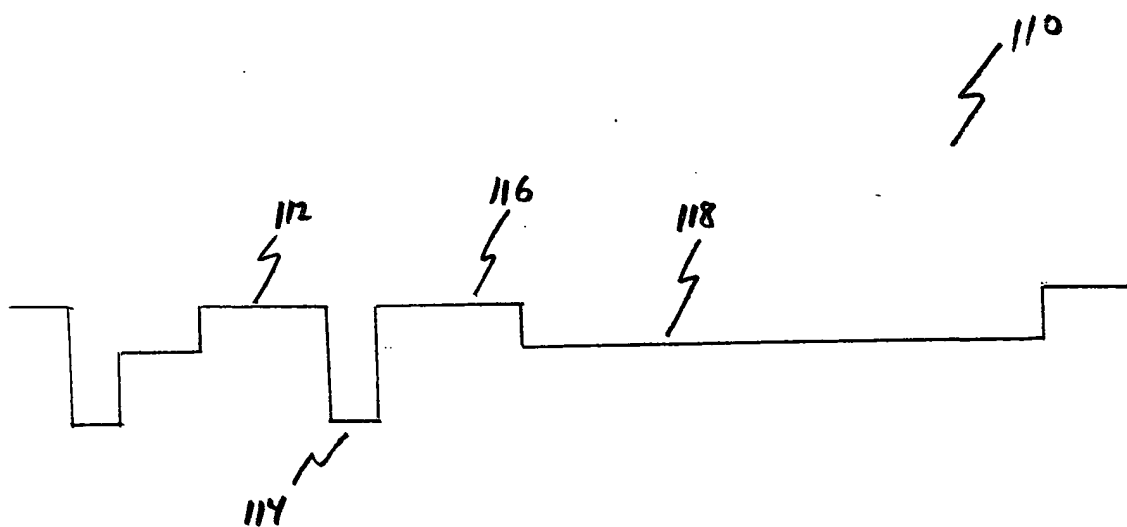


Fig. 11

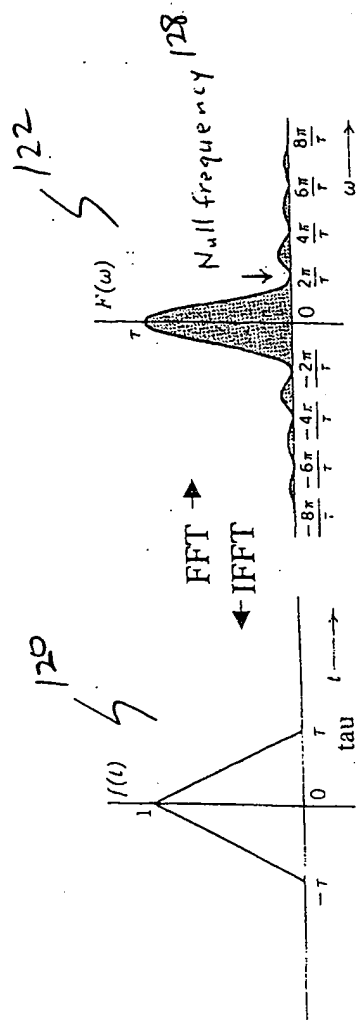


Fig. 12a

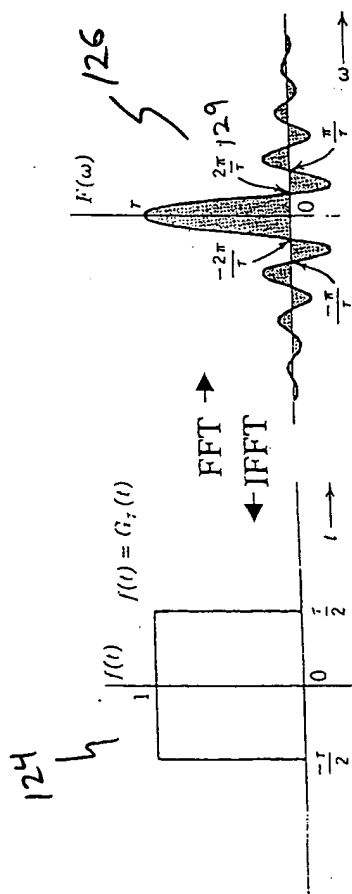


Fig. 12b

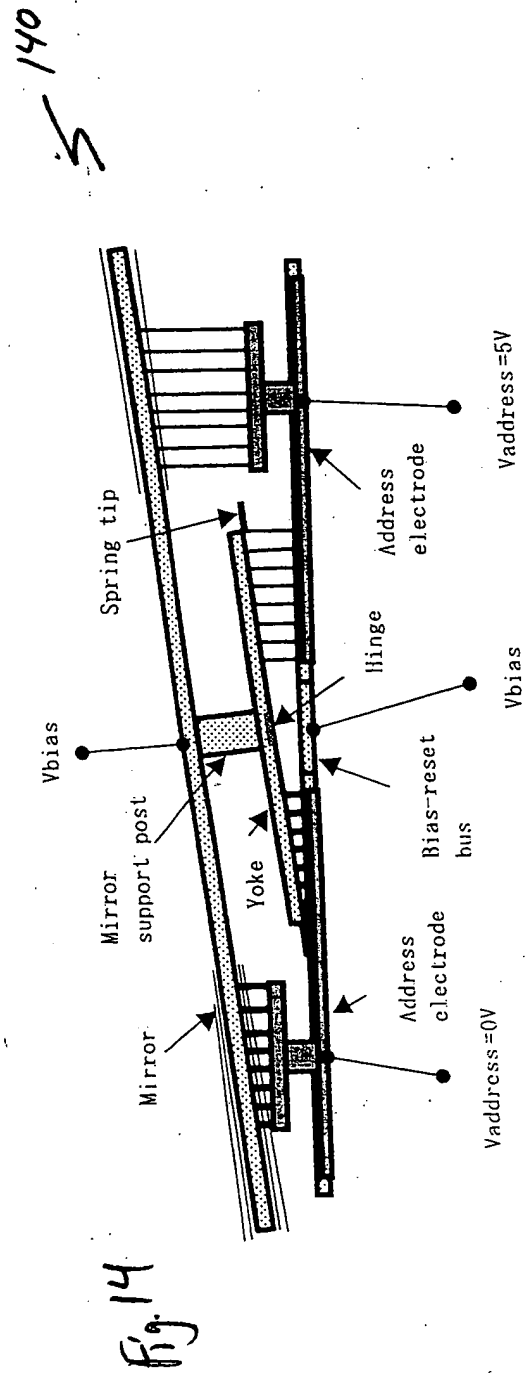
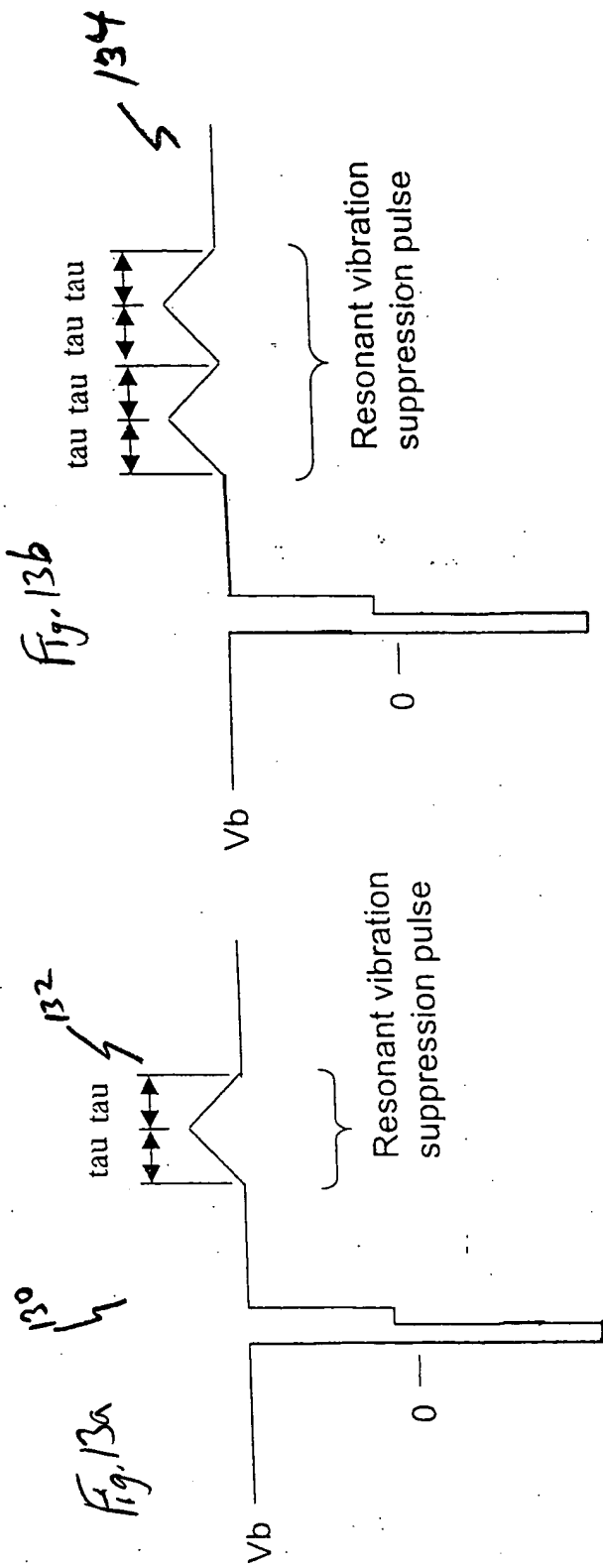


Fig. 15

